

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT Sheet 1 of 2**

Docket No. P6452

Applicant: Joseph J. Ervin
 Serial No: 09/867,129
 Filed: May 29, 2001
 For: METHOD AND APPARATUS FOR CONFIGURING MULTIPLE SEGMENT
 WIRED-AND BUS SYSTEMS
 Examiner: K. T. Huynh
 Art Unit: 2189

U.S. PATENT DOCUMENTS

Exam Inits	Cite No.	Patent Number	Kind Code	Patentee or Applicant Name	Issue Date
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		5,546,546	A	Bell, et al.	8/1996
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Examiner
Signature

Kim T. Huynh

Date
Considered

3/6/06

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Exam Initis	Cite No.	Cy	Number	Kind Code	Patentee or Applicant Name	Publication Date	T
WJ		WO	94/14121		Compaq Computer Corp.	6/23/1994	<input type="checkbox"/>
WJ		EP	0 607 733		IBM Corp	7/27/1994	<input type="checkbox"/>
WJ		WO	09/34376		Koninklijke Philips Electronics NV	8/6/1998	
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OTHER PRIOR ART – NON PATENT LITERATURE AND DOCUMENTS

Exam Initis	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the articles (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T
WJ		DELEGANES, Ellen, "Using InfiniBand Architecture to Optimize Cache Fusion in Oracle 9 Real Application Clusters (RAC)", 2001, Intel.	<input type="checkbox"/>
WJ		PHILIPS, "I2C Bus Repeater, Data Sheet", March 7, 2001, Page	<input type="checkbox"/>
WJ		ANONYMOUS, "Identical I2C Components Sharing a Unique I2C Address", September, 1998, Page 1245	<input type="checkbox"/>
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